



Applications of VHDL to Circuit Design

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Applications of VHDL to Circuit Design From Springer

Describing and designing complex electronic systems has become an overwhelming activity for which VHDL is showing increasingly useful and promising support. Although created as a description language, VHDL is being increasingly used as a simulatable and synthesizable design language. For the first time, here is a book which describes a number of unique and powerful ways VHDL can be used to solve typical design problems in systems ones which must be designed correctly in very short periods of time. Typically useful techniques such as switch-level modeling, mixed analog and digital modelling, and advanced synthesis for which VHDL shows great promise are fully presented. These methods are both immediately applicable, and indicate the potential of VHDL in efficiently modelling the real world of electronic systems. Since its inception, there has been a desire for an analog description language consistent with (and integrated with) VHDL. Until recently, VHDL could only be applied to digital circuits. The dream of describing and simulating mixed analog and digital circuits is now a reality as described herein. Describing the functionality of analog circuits including interoperability with digital circuits using the VHDL paradigm is surprisingly easy and powerful. The approach outlined by the authors presages a significant advance in the simulation of mixed systems.

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Applications of VHDL to Circuit Design From Springer Bibliography

- Published on: 1991-06-30
- Released on: 1991-06-30
- Original language: English
- Number of items: 1
- Dimensions: 9.25" h x .59" w x 6.10" l, .81 pounds
- Binding: Paperback
- 232 pages

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